Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) An energy recovering apparatus of a plasma display panel, comprising:

said plasma display panel;

a voltage source for supplying a sustain voltage to the panel;

a first inductor for recovering an energy stored in the panel into the voltage source;

a second inductor for receiving an energy from the voltage source in which the recovered energy has been stored to charge the received energy; and

switching devices for shutting off a path between the voltage source and the second inductor in a state in which energy has been stored in the second inductor to derive an inverse voltage into the second inductor and allowing said inverse voltage to be applied to the panel, wherein the voltage source includes:

a first voltage source electrically coupled between the panel and the ground voltage source; and

a second voltage source connected between the first voltage source and the ground voltage source..

2. (Canceled).

3. (Currently Amended) The energy recovering apparatus as claimed in claim ~~2~~1, wherein each of the first and second voltage sources has a voltage value equal to a half of the sustain voltage.

4. (Original) The energy recovering apparatus as claimed in claim 1, further comprising:

a first switch for forming a path between the voltage source and the panel such that said sustain voltage of the voltage source is supplied to the panel;

a second switch for forming a path among the panel, the first inductor and the voltage source such that an energy from the panel is recovered into the voltage source; and

a first diode connected between the second switch and the panel.

5. (Currently Amended) The energy recovering apparatus as claimed in claim ~~2~~1, wherein the switching devices include:

a ~~third~~ first switch connected between a node positioned between the second inductor and the panel and the ground voltage source; and

a ~~fourth~~ second switch connected between the second inductor and the second voltage source.

6. (Currently Amended) The energy recovering apparatus as claimed in claim 5, wherein the switching devices further include:

a ~~second~~ first diode connected between the ~~fourth~~ second switch and the second inductor; and

a ~~third~~ first diode connected between a node positioned between the ~~fourth~~ second switch and the second diode and the ~~third~~ first switch.

7. (Currently Amended) The energy recovering apparatus as claimed in claim 5, wherein said inverse voltage is generated when the ~~third~~ first and ~~fourth~~ second switches are turned off in a turned-on state.

8. (Currently Amended) The energy recovering apparatus as claimed in claim 6, wherein the ~~second~~ first and ~~third~~ second diodes form a path between the second inductor and the panel such that said inverse voltage is supplied to the panel.

9. (Currently Amended) An energy recovering method for a plasma display panel, comprising ~~the steps of~~:

(A) supplying a sustain voltage from a voltage source to the panel;

(B) recovering an energy stored in the panel into the voltage source using a first inductor;

(C) receiving an energy from the voltage source in which the recovered energy has been stored to thereby charge the energy into a second inductor; and

(D) shutting off a path between the voltage source and the second inductor in a state in which an energy has been stored in the second inductor using the switching devices to derive an inverse voltage into the second inductor and applying said inverse voltage to the panel,

wherein said step (A) includes:

forming a path between the first and second voltage sources and the panel connected in series using a first switch to thereby apply voltages from the first and second voltage sources to the panel.

10. (Canceled).

11. (Currently Amended) The energy recovering method as claimed in claim ~~10~~9, wherein said ~~(B)~~ step (B) includes:

forming a path between the panel and the second voltage source going by way of the first inductor using a second switch connected between the first inductor and the panel to thereby recover energy of the panel into the second voltage source.

12. (Currently Amended) The energy recovering method as claimed in claim ~~10~~9, wherein said ~~(C)~~ step (C) includes:

forming a path between the second voltage source and the second inductor using a third switch connected between the second voltage source and the second inductor and a fourth switch connected between the second inductor and a ground voltage source.

13. (Original) The energy recovering method as claimed in claim 12, wherein said inverse voltage is generated when the third and fourth switches are turned off in a turned-on state.

14. (Currently Amended) The energy recovering method as claimed in claim 12, wherein said ~~(D)~~ step (D) includes:

forming a path among the second inductor, a panel capacitor, a second diode, a first diode and the second inductor using the first diode connected between the third switch and the second inductor and the second diode connected between a node positioned between the first diode and the third switch and the ground voltage source.

15. (New) An energy recovery circuit for a plasma display panel having a panel capacitance, comprising:

a first node for coupling to the panel capacitance;

a second node for coupling to a voltage source;

a first inductor having first and second electrodes, the first inductor being coupled between the first node and the second node;

a second inductor having first and second electrodes, the first electrode of the second inductor being coupled to the first node, and the second electrode of the inductor being coupled to a third node;

a fourth node for coupling to a prescribed voltage;

a first transistor having first and second electrodes, the first electrode of the first transistor being coupled to the first node, and the second electrode of the first transistor being coupled to the fourth node; and

a second transistor having first and second electrodes, the first electrode of the second transistor being coupled to the second node, and the second electrode of the second transistor being coupled to the third node, wherein

the first and second transistors are turned off during a first prescribed period of time such that a first electrically conductive path is provided through the first node, the fourth node and the third node such that energy stored in the second inductor is provided to the panel capacitance.

16. (New) The energy recovery circuit of claim 15, further comprising a third transistor having first and second electrodes, the first electrode of the third transistor being coupled to the first node, wherein the third transistor is turned on and first and second transistors are turned off during a second prescribed period of time such that a second electrically conductive path is provided through the first node, fourth node and the second node.

17. (New) The energy recovery circuit of claim 16, further comprising a fourth transistor coupled between the first and second nodes, the fourth transistor having first and second electrodes, the first electrode of the first inductor being coupled to the second node and the second electrode of the first inductor and the second node of the fourth transistor being coupled to each other, wherein during a third prescribed period of time, the fourth transistor is turned on while the first, second and third transistors are turned off such that a third electrically conductive path is formed through the first, second and fourth nodes to allow removal of energy stored in the first inductor.

18. (New) The energy recovery circuit of claim 17, wherein during a fourth prescribed period of time, the first and second transistors are turned on while the third and fourth transistors are turned off such that a fourth electrically conductive path is formed through the first, fourth and second nodes to store energy in the second inductor.

19. (New) The energy recovery circuit of claim 18, further comprising:

a first diode coupled between the second inductor and the third node;

a second diode coupled between the first node and the fourth transistor; and

a third diode coupled between the third and fourth nodes.

20. (New) The energy recovery circuit of claim 16, wherein the voltage source comprises a first voltage source coupled to the second node and the second electrode of the third electrode, and a second voltage source coupled to second node and the fourth node, the first and second voltage source having a voltage value equal to each other.

21. (New) The energy recovery circuit of claim 15, wherein the prescribed voltage is a ground voltage.